



PATENT

IN THE UNITED STATES PATENT OFFICE

COPY

Applicants: Raymond C. Pang et al.
Assignee: Xilinx, Inc.
Title: "Programmable Logic Device with Method of Preventing Readback"

Serial No.: 09/724,975 File Date: November 28, 2000
Examiner: Unknown Art Unit: 2818
Docket No.: X-805-6 US Conf. No.: 7824

COMMISSIONER FOR PATENTS
P.O. BOX 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the four (4) references listed in the attached Form PTO-1449. A copy of each is enclosed herein.

These references were cited in related foreign application. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

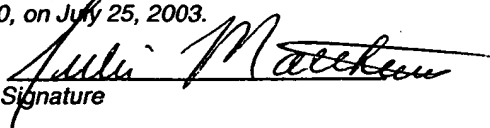
Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,


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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 25, 2003.

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